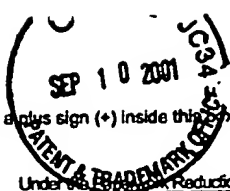


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Sheet 1 of 2

Complete If Known

Application Number	09/847,642
Filing Date	May 1, 2001
First Named Inventor	Mihai T. Lazarescu
Group Art Unit	2124 2124
Examiner Name	INGBERG INGBERG
Attorney Docket Number	261/246

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
RGT I	AA	Prof. Dov Dori, <u>About OPCAT</u> , 2000, pages 1-2, http://iew3.technion.ac.il/~dori/opcat/about.htm1	
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Application Number	09/847,642
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Group Art Unit	2124 2124
Examiner Name	2124 IUGERL
Attorney Docket Number	261/246

Sheet	2	of	2
PLT	AT	Soner Onder, Rajiv Gupta, <u>Automatic Generation of Microarchitecture Simulators</u> , May 1998, IEEE International Conference on Computer Languages	
PLT	AU	Joachim Fitzner, Chris Schlager, Davorin Mista, Vojin Zivojnovic, <u>Implementing LISA Tools Based on a DSP Architecture Description</u> , ICSPAT 1999	
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PLT	AX	Chris Schlager, Joachim Fitzner, Vojin Zivojnovic, <u>Using Supersim Compiled Processor Models for Hardware, Software and System Design</u> , ICSPAT 1998	
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Examiner Signature		Date Considered	5/10/05
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